

# Application of thermal resistance network model in optimization design of micro-channel cooling heat sink

Baodong Shao, Lifeng Wang and Jianyun Li Department of Engineering Mechanics, College of Architectural and Civil Engineering, Kunming University of Science and Technology, Kunming, People's Republic of China, and

Zhaowei Sun

Research Institute of Satellite Technology, Harbin Institute of Technology, Harbin, People's Republic of China

## Abstract

**Purpose** – The purpose of this paper is to optimize the configuration sizes of micro-channel cooling heat sink using the thermal resistance network model. The optimized micro-channel heat sink is simulated by computational fluid dynamics method, and the total thermal resistance is calculated to compare with that of thermal resistance network model.

**Design/methodology/approach** – Taking the thermal resistance and the pressure drop as goal functions, a multi-objective optimization model was proposed for the micro-channel cooling heat sink based on the thermal resistance net work model. The Sequential Quadratic Programming procedure was used to do the optimization design of the structure size of the micro-channel. The optimized micro-channel heat sink was numerically simulated by computational fluid dynamics (CFD) software. **Findings** – For the heat sink to cool a chip with the sizes of  $L \times W = 2.5 \text{ mm} \times 2.5 \text{ mm}$  and the power of 8 W, the optimized width and height of the micro-channel are 154 µm and 1,000 µm, respectively, and its corresponding total thermal resistance is 8.255 K/W. According to the simulation results, the total thermal resistance of whole micro-channel heat sink  $R_{total}$  is 7.596 K/W, which agrees well with the analysis result of thermal resistance network model.

**Research limitations/implications** – The convection heat transfer coefficient is calculated approximatively here for convenience, and that may induce some errors.

**Originality/value** –The maximum difference in temperature of the optimized micro-channel cooling heat sink is 59.064 K, which may satisfy the requirement for removal of high heat flux in new-generation chips. **Keywords** Thermal resistance, Fluid dynamics, Optimum design, Programming

Paper type Research paper

# 1. Introduction

With the developments of international aerospace technology, micro-electromechanical system and micro-machining technology, to transfer the heat generated by the micro-electronic chips to keep the stable and reliable operation of the devices is present problem to be solved in micro-electronic industry (Zeng-Yuan, 1988). When the heat flux of micro-electronic devices exceeds 100 W/cm<sup>2</sup>, traditional cooling method is unlikely to meet the cooling needs (Kleiner *et al.*, 1995), and now micro-channel cooling

The authors acknowledge the financial support of special funds of College of Architectural and

Civil Engineering, Kunming University of Science and Technology.

Emerald

International Journal of Numerical Methods for Heat & Fluid Flow Vol. 19 Nos. 3/4, 2009 pp. 535-545 © Emerald Group Publishing Limited 0961-5539 DOI 10.1108/09615530910938425

Thermal resistance network model

535

Received 15 October 2007 Revised 28 February 2008 Accepted 28 February 2008 HFF 19.3/4

536

heat sink is an effective kind of substitute method. Tuckerman and Pease (1981a, b) first proposed that micro-channel heat sink can be used to cool electronic devices, and studied cooling performance of micro-channels directly fabricated into the back of silicon wafers of electronic chip. They found that the frictional resistance coefficient of liquid in the micro-channels was higher than that predicted by classical theory. The primary reason that affects the heat transfer performance in micro-channel cooling heat sink is the total thermal resistance of heat sink, and thermal resistance network model is an effective analysis method for thermal resistance. Wei and Joshi (2003) developed a simple thermal resistance network model to evaluate the overall thermal performance of a stacked micro-channel heat sink, and the aspect ratio, fin thickness and the ratio of channel width to fin thickness was optimized based on the model. Skandakumaran *et al.* (2004) analyzed the thermal resistance of single and multi-layer micro-channel heat sinks with the thermal resistance network model. Chong et al. (2002) modeled a single layer counter flow and a double layer counter flow microchannel heat sink with rectangular channels by employing the thermal resistance network, and the accuracy of the prediction was verified by comparing the results obtained with those from the more comprehensive three dimensional CFD conjugate heat transfer model, and good agreements were obtained. The results showed that the overall thermal resistance was related with configuration sizes of micro-channel heat sink. Shao et al. (2007) optimized the cross-section sizes of micro-channels, the heat flux of chip is 278 W/cm<sup>2</sup>, and through the optimization micro-channel cooling heat sink, the highest temperature in the chip could be kept below 42 °C. Quadir et al. (2001) applied a finite-element method to evaluate the performance of micro-channel heat exchangers, and the methodology was able to predict the surface temperature, the fluid temperature and the total thermal resistance of the micro-channel heat sink. Liu and Garimella (2005) provided modeling approaches of increasing levels of complexity for the analysis of convective heat transfer in micro-channels which offer adequate descriptions of the thermal performance. Hegde et al. (2005) analyzed two-phase flow in micro-channel heat exchangers by using the finite-element method to solve the energy balance equations developed for two-phase flow in micro-channels. Jeevan et al. (2005) used the genetic algorithms under different flow constraints to determine the optimal dimensions for a stacked micro-channel, and the 2D FEM analysis resulted in lower thermal resistance. Massarotti et al. (2003) investigated microscopic and macroscopic approaches to the solution of natural convection in enclosures filled with fluid saturated porous media, and at the microscopic level, the porous medium was represented by different assemblies of cylinders and the Navier-Stokes equations were assumed to govern the flow.

The heat transfer performance of micro-channel cooling heat sink is affected by the flow state of liquid in micro-channel, besides the thermal resistance. Different thermophysical properties and velocity for different liquid result different flow state, and different flow state results different heat transfer effect. Flow of liquid can be expressed as pressure drop, and pressure drop is related with configuration sizes of micro-channel heat sink. Therefore, based the effect of configuration of micro-channel heat sink on the thermal resistance and pressure drop, which can be as goal function, the problem becomes multi-objective optimization. The sequential quadratic programming (SQP) procedure was used to do the optimization design of the configuration size of the micro-channel. The optimized micro-channel heat sink is simulated by CFD method, and the total thermal resistance is calculated to compare with that of thermal resistance network model.

## 2. Physical model and computational zone

A micro-channel cooling heat sink is shown in Figure 1, which is used to cool a chip with the size of  $W \times L = 2.5 \,\mathrm{mm} \times 2.5 \,\mathrm{mm}$ , and the power is 8 W, so the corresponding heat flux is  $128 \,\mathrm{W/cm^2}$ . The working fluid is deionized water. In the Figure 1,  $W_c$  is the width of micro-channel,  $H_c$  is the depth of micro-channel,  $W_w$  is the width of fin, and  $H_{\rm sub}$  is the thickness of substrate. For the symmetry of the structure of model and load, the computational zone can be half of micro-channel and fin, and the schematic diagram of computational zone cross section is shown in Figure 2.

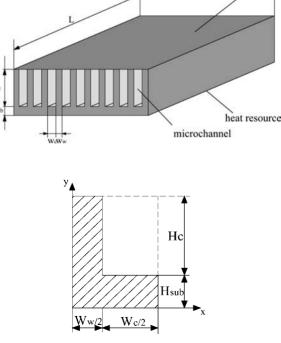
## 3. Thermal resistance network model

In micro-channel cooling heat sink, the total resistance  $R'_{total}$  includes the substrate conduction thermal resistance  $R_{sub}$ , the wall conduction thermal resistance  $R_{wall}$ , the substrate convection thermal resistance  $R_{sub,conv}$ , the wall convection thermal resistance  $R_{wall,conv}$  and the liquid flow thermal resistance  $R_{fluid}$ . For the computational zone in Figure 2, the original thermal resistance network model and the equivalent thermal resistance network model are shown in Figure 3.

Thus, the total thermal resistance  $R'_{total}$  of half micro-channel and fin can be expressed as:

$$R'_{total} = R_{sub} + \left(\frac{1}{R_{sub,conv}} + \frac{1}{R_{wall} + R_{wall,conv}}\right)^{-1} + R_{fluid}$$
(1)

pyrex



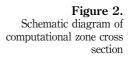
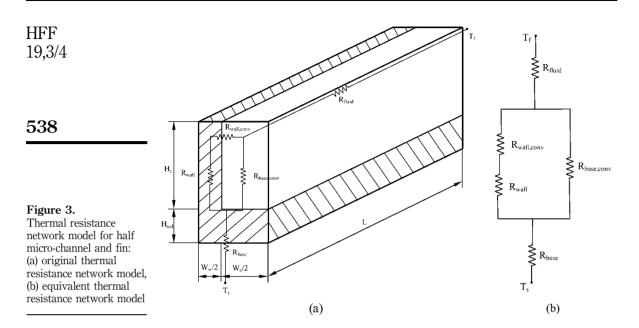


Figure 1.

heat sink

Schematic diagram of three-dimension rectangle micro-channels cooling



Where the substrate conduction thermal resistance  $R_{sub}$  is

$$R_{sub} = \frac{H_{sub}}{k_s((W_c/2) + (W_w/2))L}$$
(2)

The wall conduction thermal resistance  $R_{wall}$  is

$$R_{wall} = \frac{H_c}{k_s(W_w/2)L} \tag{3}$$

The wall convection thermal resistance Rwall,conv is

$$R_{wall,conv} = \frac{1}{h_{conv}H_cL} \tag{4}$$

The substrate convection thermal resistance  $R_{sub,conv}$  is

$$R_{sub,conv} = \frac{1}{h_{conv}(W_c/2)L}$$
(5)

The liquid flow thermal resistance  $R_{fluid}$  is

$$R_{fluid} = \frac{1}{\dot{m}C_b} \tag{6}$$

In expressions (2)-(6),  $K_s$  is the thermal conductivity of the substrate,  $\dot{m}$  is the mass flow rate of working fluid in half of micro-channel,  $C_p$  is the specific heat of the working fluid and  $h_{conv}$  is the convective heat transfer coefficient.

$$h_{conv} = \frac{Nu \cdot k_l}{D_h}$$
Thermal  
resistance  
network model

539

where  $K_l$  is the thermal conductivity of working fluid,  $D_n$  is hydraulic diameter of cross section, for half of rectangle micro-channel,  $D_h = 4A/P = W_cH_c/(W_c/2 + H_c)$ , where A is cross-sectional area of half of micro-channel, P is wetted perimeter of half of micro-channel. When Re is less than 1,000, Nu can be express with experimental formula as (Kim and Kim, 1991)

$$Nu = 2.253 + 8.164 \left(\frac{\alpha}{\alpha+1}\right)^{1.5}$$

where  $\alpha$  is ratio of height to width of micro-channel,  $\alpha = H_c/W_c$ .

Re can be calculated with the formula (7)

$$\operatorname{Re} = \frac{\rho_l u D_h}{\nu} \tag{7}$$

where  $\nu$  is dynamic viscosity,  $\rho_l$  is density of liquid, u is velocity of liquid. Considered the effect of temperature on dynamic viscosity, the formula can be used to calculate the dynamic viscosity of liquid (Incorpera, 1999).

$$\nu = 2.414 \times 10^{-5} \cdot 10^{247.8/(T-140)} \tag{8}$$

According to which (1), the total thermal resistance of the whole micro-channel heat sink can be expressed as

$$R_{total} = \frac{R'_{total}}{2n} \tag{9}$$

Where *n* is the number of micro-channels.

Besides thermal resistance, pressure drop affects the heat transfer performance of micro-channel heat sink (Wu and Cheng, 2003).

$$\Delta P = 2f \rho_l u_{ave}^2 \frac{L}{D_h} \tag{10}$$

Where  $u_{ave}$  is the mean velocity of liquid, *f* is friction coefficient. For convenience of calculation (Li *et al.*, 2004), *f* Re  $\approx$  68, when Re < 200.

#### 4. Multi-objective optimization method

Multi-objective optimization problem deals with optimization problem to solve objective vector F(x), generally with constraints, the standard form is

$$\min_{\mathbf{x}\in R^n}\mathbf{F}(\mathbf{x}) = \min_{\mathbf{x}\in R^n}(f_1(\mathbf{x}), f_2(\mathbf{x}), \dots, f_m(\mathbf{x}))$$

where

HFF  
19,3/4  
$$\mathbf{G}_{i}(\mathbf{x}) = 0 \quad i = 1, \dots, m_{e}$$
$$\mathbf{G}_{i}(\mathbf{x}) \leq 0 \quad i = m_{e} + 1, \dots, m$$
$$lb \leq \mathbf{x} \leq ub$$

The base idea to solve multi-objective optimization problem is to structure a merit function from each subgoal function, and translate multi-objective optimization problem into single-objective optimization problem to solve merit function with goal attainment programming. The problem formulation allows the objectives to be underor overachieved, enabling the designer to be relatively imprecise about initial design goals. The relative degree of under- or overachievement of the goals is controlled by a vector of weighting coefficients,  $\mathbf{w} = (w_1, w_2, \ldots, w_m)$ , and is expressed as a standard optimization problem using the following formulation.

$$\min_{\gamma \in R, \mathbf{x} \in R^n} \quad \gamma \tag{11}$$

such that  $F_i(\mathbf{x}) - w_i \gamma \le f_i^*, i = 1, 2, ..., m$ 

In goal attainment programming there might be a more appropriate merit function, which you can achieve as the minimax problem.

$$\min_{\mathbf{x}\in\mathbb{R}^n}\max_i\{\Lambda_i\}\tag{12}$$

where

540

$$\Lambda_i = \frac{F_i(\mathbf{x}) - f_i^*}{w_i}, \quad i = 1, 2, \dots, m$$

Different optimization algorithm has different merit function. In SQP, the merit function Equation (12) can be written as

$$\psi(\mathbf{x},\gamma) = \gamma + \sum_{i=1}^{m} r_i \cdot \max\{0, F_i(\mathbf{x}) - w_i\gamma - f_i^*\}$$
(13)

When the merit function of Equation (13) is used as the basis of a line search procedure, then, although  $\psi(\mathbf{x}, \gamma)$  might decrease for a step in a given search direction, the function max  $\Lambda_i$  might paradoxically increase. A solution is therefore to set  $\psi(\mathbf{x})$  equal to be

$$\psi(\mathbf{x}) = \sum_{i=1}^{m} \begin{cases} r_i \cdot \max\{0, F_i(\mathbf{x}) - w_i\gamma - f_i^*\} & if \\ \max_i \Lambda_i, i = 1, 2, \cdots, m & otherwise \end{cases}$$
(14)

Suitable goal function and weighting coefficient are selected, multi-objective optimization problem can be optimized by using merit function Equation (14).

#### 5. Optimization design

Select the number of micro-channel *n*, the width of micro-channel  $W_c$ , the width of fin  $W_w$  and the height of micro-channel  $H_c$  as design variable, expressed as  $\mathbf{x}_1$ ,  $\mathbf{x}_2$ ,  $\mathbf{x}_3$  and

 $\mathbf{x}_4$  respectively, and written in vector  $\mathbf{x} = [x_1, x_2, x_3, x_4]$ . The formula (1) and (10) are goal functions, and can be written in function of  $\mathbf{x}$ , as  $f_1(\mathbf{x})$  and  $f_2(\mathbf{x})$ . The width of heat sink are constraint, and the boundary of variables are construct, the multi-objective optimization problem is

Thermal resistance network model

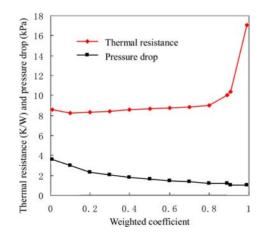
541

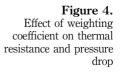
$$\begin{cases} Find & \mathbf{x} \\ \min & F(\mathbf{x}) = \min\{f_1(\mathbf{x}), f_2(\mathbf{x})\} \\ s.t. & x_1 x_2 + (x_1 + 1) x_3 - 0.0025 = 0 \\ & 5 \le x_1 \le 300 \\ & 5 \times 10^{-5} \le x_2 \le 10^{-3} \\ & 2 \times 10^{-5} \le x_3 \le 10^{-3} \\ & 5 \times 10^{-5} \le x_4 \le 10^{-3} \end{cases}$$
(15)

The goal attainment SQP method is used to optimize the above problem. At each major iteration, an approximation is made of the Hessian of the Lagrangian function using a quasi-Newton updating method. This is then used to generate a QP subproblem whose solution is used to form a search direction for a line search procedure.

## 6. Optimization results and discussion

Initial value does not affect optimization results significantly, but suitable initial value can decrease iteration time. The initial value is chosen as  $\mathbf{x}_0 = [10, 1 \times 10^{-4}, 8 \times 10^{-5}, 5 \times 10^{-4}]$  in this paper, the objective value of thermal resistance and pressure drop is  $F^* = [9, 1]$ , the units are K/W and 100 Pa, respectively. The weighting coefficient is chosen according to the relative importance between thermal resistance and pressure drop. Figure 4 shows the effect of weighting coefficient on thermal resistance is the total thermal resistance of the whole micro-channel heat sink. The thermal resistance increases with weighting coefficient, and pressure drop decreases with weighting coefficient, which indicates that the thermal resistance is less when the thermal resistance is more important than pressure drop. When the weighting coefficient is

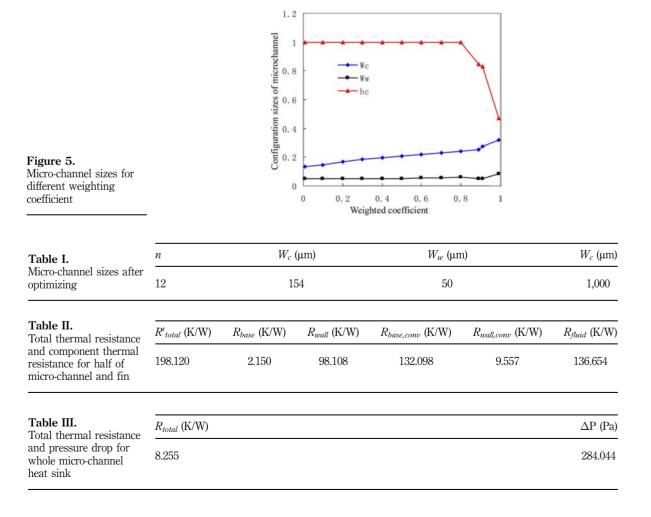




HFF 19,3/4	about 0.1, the thermal resistance is least, and corresponding pressure drop is about 280 Pa, and corresponding configuration sizes of micro-channel are listed in Table I. The total and component thermal resistance is listed in Table II, which show that the
542	substrate conduction thermal resistance is minimal in total thermal resistance. The main reason is that micro-channels are fabricated directly into the back of chip, which eliminates the thermal contact resistance between micro-channel heat sink and chip. The liquid flow thermal resistance is maximum in total thermal resistance, which because the velocity of liquid is very low for the restriction of pump power. The total thermal resistance and pressure drop for whole micro-channel heat sink are listed in Table III.

# 7. Numerical simulation

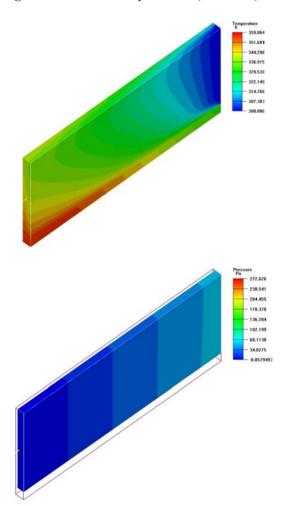
The numerical simulation is used to verify the cooling performance of optimal microchannel cooling heat sink by using electronics cooling software that has been previously used in analyzing the heat transfer character in electronics and chip cooling applications, which uses finite-volume method to solve CFD problem. For simplicity of



computational zone, hexahedron structured grids are used to mesh the computational zone. First order upwind scheme is used to discrete control equations, and semi-implicit method for pressure-linked equations is used to solve discretization equations.

Restricted by the power of pump, the inlet velocity of working fluid is 1 m/s, the density of working fluid is  $\rho_f = 997 \text{ kg/m}^3$ , the specific heat is  $C_p = 4,179 \text{ J/kgK}$ , the dynamic viscosity is calculated by Equation (8), the height between the chip surface and the base of the micro-channel  $h_{sub}$  is  $100 \,\mu\text{m}$ , the dimension of chip is  $L \times W = 2.5 \,\text{mm} \times 2.5 \,\text{mm}$ , and the thermal conductivity of the substrate  $K_s$  and working fluid  $K_f$  are 148 and 0.613 W/mK. The Reynolds number estimated by the above conditions is less than 200, therefore the flow is laminar.

Figure 6 shows the temperature distribution of optimized micro-channel for half channel and fin. The maximum difference in temperature is 59.064 K, and the transferred power of heat flux is 0.324 W, so the total thermal resistance  $R'_{total}$  is 182.296 K/W, and the total thermal resistance of whole micro-channel heat sink  $R_{total}$  is 7.596 K/W, which agree well with the analysis result (8.255 K/W) of thermal resistance



Thermal resistance network model

543

Figure 6. Temperature distribution of optimized micro-channel

Figure 7. Pressure distribution of optimized micro-channel

network model, and the relative error is 7.983 per cent. Figure 7 shows the pressure distribution of optimized micro-channel for half channel and fin. The pressure drop of 19.3/4liquid in micro-channel is 272.626 Pa, which agree well with the analysis result (284.044 Pa), and the relative error is 4.020 per cent.

#### 8. Conclusion

HFF

544

Thermal resistance network model is used to analyze the thermal resistance of microchannel heat sink, and establish thermal resistance network model for half microchannel and fin. Based on the model and pressure drop formula, the multi-objective optimization model of micro-channel heat sink is found, and SQP is used to optimize the configuration sizes of micro-channel. The height and width of optimized microchannel are 1,000 and 154 µm, and the ratio is 6.49, which is less than 10 (Ryu *et al.*, 2002). The thermal resistance of whole micro-channel heat sink is 8.255 K/W. To verify the optimization results, CFD numerical simulation method is used, and the results agree well with analysis results.

## References

- Chong, S.H., Ooi, K.T. and Wong, T.N. (2002), "Optimization of single and double layer counter flow microchannel heat sinks". Applied Thermal Engineering, Vol. 22, pp. 1569-85.
- Hegde, P., Seetharamu, K.N., Quadir, G.A., Aswathanarayana, P.A., Abdullah, M.Z. and Zainal, Z.A. (2005), "Thermal analysis of micro-channel heat exchangers with two-phase flow using FEM", International Journal of Numerical Methods for Heat & Fluid Flow, Vol. 15 No. 1, pp. 43-60.
- Incorpera, F. (1999), Liquid Cooling of Electronic Devices by Single-Phase Convection, Wiley, New York, NY.
- Jeevan, K., Quadir, G.A., Seetharamu, K.N., Azid, I.A. and Zainal, Z.A. (2005), "Optimization of thermal resistance of stacked micro-channel using genetic algorithms", International Journal of Numerical Methods for Heat & Fluid Flow, Vol. 15 No. 1, pp. 27-42.
- Kim, S.J. and Kim, D. (1991), "Forced convection in microstructures for electronic equipment cooling", Journal of Heat Transfer, Vol. 21, pp. 639-45.
- Kleiner, M.B., Kuehn, S.A. and Haberger, K. (1995), "High performance forced air cooling scheme employing micro channel heat exchangers", IEEE Transactions on Components, Packaging and Manufacturing Technology Part A, Vol. 18 No. 4, pp. 795-804.
- Li, J., Peterson, G.P. and Cheng, P. (2004), "Three-dimensional analysis of heat transfer in a microheat sink with single phase flow", International Journal of Heat and Mass Transfer, Vol. 47, pp. 4215-31.
- Liu, D. and Garimella, S.V. (2005), "Analysis and optimization of the thermal performance of microchannel heat sinks", International Journal of Numerical Methods for Heat & Fluid Flow, Vol. 15 No. 1, pp. 7-26.
- Massarotti, N., Nithiarasu, P. and Carotenuto, A. (2003), "Microscopic and macroscopic approach for natural convection in enclosures filled with fluid saturated porous medium", International Journal of Numerical Methods for Heat & Fluid Flow, Vol. 13 No. 7, pp. 862-86.
- Quadir, G.A., Mydin, A. and Seetharamu, K.N. (2001), "Analysis of microchannel heat exchangers using FEM", International Journal of Numerical Methods for Heat & Fluid Flow, Vol. 11 No. 1, pp. 59-76.
- Ryu, J.H., Choi, D.H. and Kim, S.J. (2002), "Numerical optimization of the thermal performance of a microchannel heat sink", International Journal of Heat and Mass Transfer, Vol. 45, pp. 2823-7.

- Shao, B.D., Sun, Z.W. and Wang, L.F. (2007), "Optimization design of microchannel cooling heat sink", *International Journal of Numerical Methods for Heat & Fluid Flow*, Vol. 17 No. 6, pp. 628-37.
- Skandakumaran, P., Ortega, A., Jamal-Eddine, T., et al. (2004), "Multi-layered SiC microchannel heat sink-modeling and experiment", 2004 International Society Conference on Thermal Phenomena, pp. 352-60.
- Tuckerman, D.B. and Pease, R.F.W. (1981a), "High-performance heat sinking for VLSI", IEEE Electron Device Letter, Vol. 2 No. 5, pp. 126-9.
- Tuckerman, D.B. and Pease, R.F.W. (1981b), "Ultrahigh thermal conductance microstructures for cooling integrated circuits", *Proceedings of 32nd Electronics Components Conference*, pp. 145-50.
- Wei, X.-J. and Joshi, Y. (2003), "Optimization study of stacked micro-channel heat sinks for microelectronic cooling", *IEEE Transactions on Components and Packaging Technologies*, Vol. 26 No. 1, pp. 55-61.
- Wu, H.Y. and Cheng, P. (2003), "An experimental study of convective heat transfer in silicon microchannels with different surface conditions", *International Journal of Heat and Mass Transfer*, Vol. 46 No. 14, pp. 2547-56.
- Zeng-Yuan, G. (1988), "Focus of current world heat transfer field-cooling of microelectronics devices", *Chinese Science Fund*, No. 2, pp. 20-5.

## **Corresponding author**

Baodong Shao can be contacted at: shbd\_1221@163.com; shbd\_1221@hotmail.com

To purchase reprints of this article please e-mail: **reprints@emeraldinsight.com** Or visit our web site for further details: **www.emeraldinsight.com/reprints**  Thermal resistance network model